

INFORMATION DISCLOSURE CITATION		ATTY. DOCKET NO.	SERIAL NO.
		50-524	10/779,805
		APPLICANT	
		MUDGE et al	
		FILING DATE	TC/A.U.
		February 18, 2004	2133

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

DOCUMENT		DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
SMJ	WO 00/54410	9/2000	WIPO				
QWS	0 366 331	5/1990	EP				
QWS	0 374 420	6/1990	EP				
QWS	0 653 708	5/1995	EP				
QWS	60-20398	2/1985	JP			ABSTRACT	
SMJ	62-24498	2/1987	JP			ABSTRACT	
SMJ	809350	2/1981	SU			ABSTRACT	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

*Examiner		8/20/02 Date Considered	8/20/02
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Sheet 1 of 1

INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO. <u>550-524</u>	SERIAL NO. <u>10/779,805</u>
APPLICANT <u>MUDGE et al</u>		
(Use several sheets if necessary)	FILED DATE <u>MAR 21 2006</u>	TC/A.U. <u>February 18, 2004</u>
		<u>2133</u>

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ARM710 Data Sheet 12/1994, Advanced RISC Machines Ltd. (ARM)
F. Worm et al, "An Adaptive Low-Power Transmission Scheme for On-Chip Networks" ISSS'02, 10/2002, pages 92-100

***Examiner**

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